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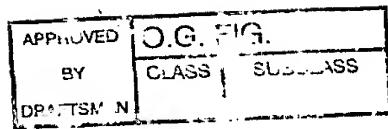


Fig. 1

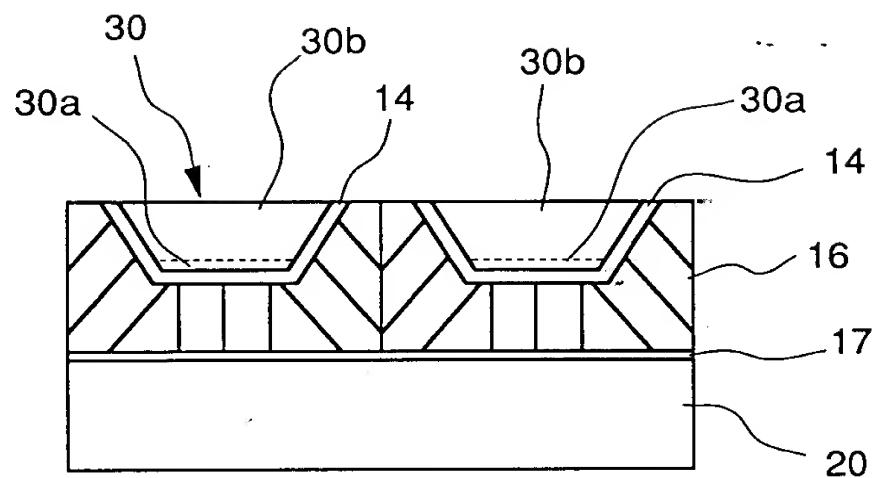
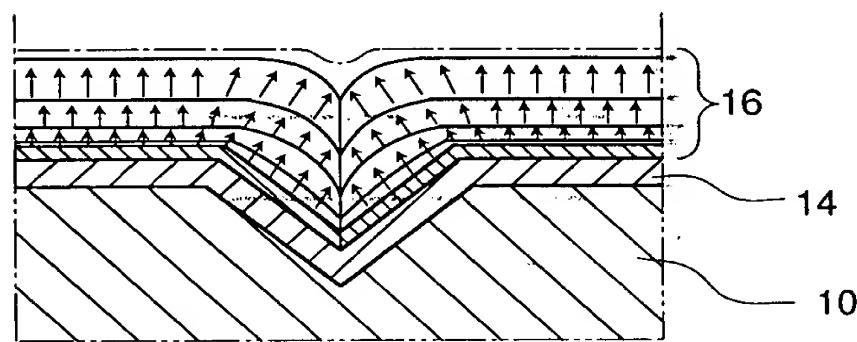
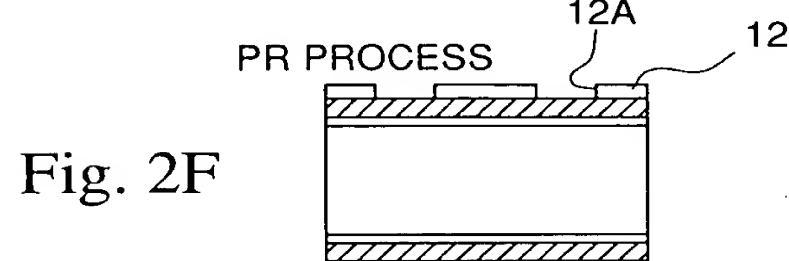
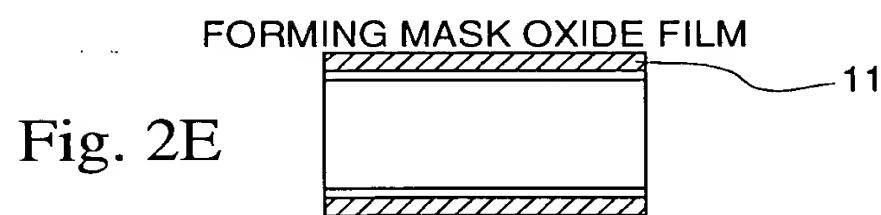
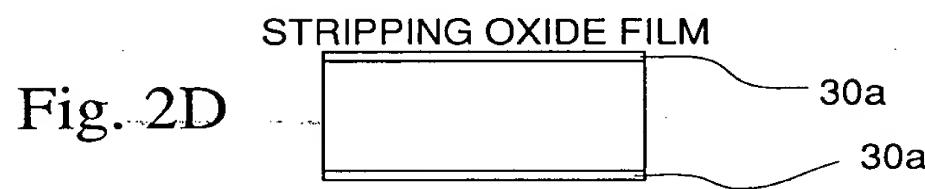
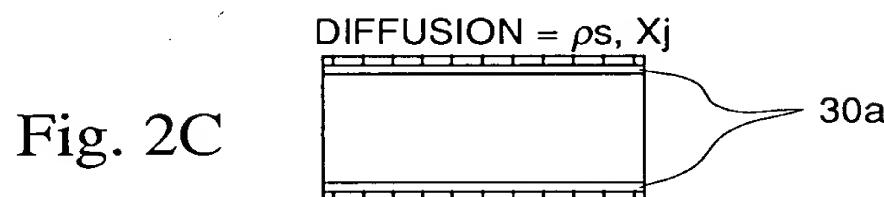
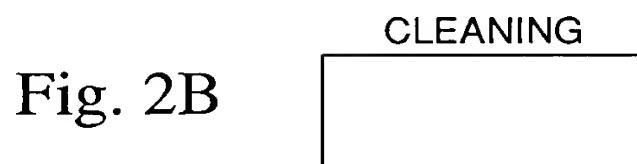
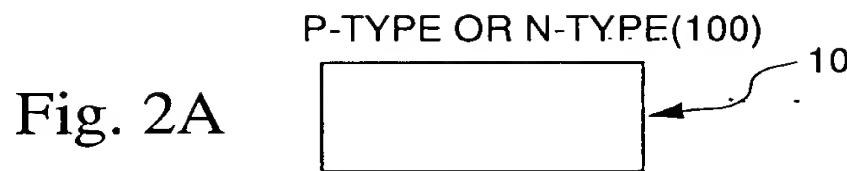


Fig. 6



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
FT-		





OPENING A WINDOW 11A

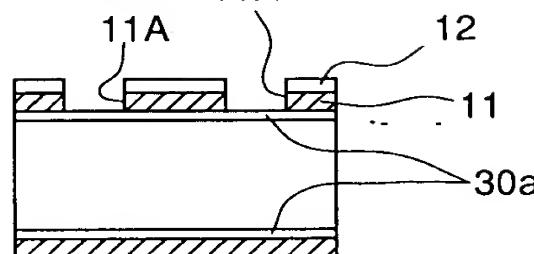


Fig. 3A

STRIPPING THE RESIST

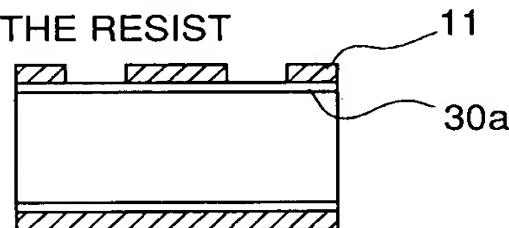


Fig. 3B

ANISOTROPIC ETCHING

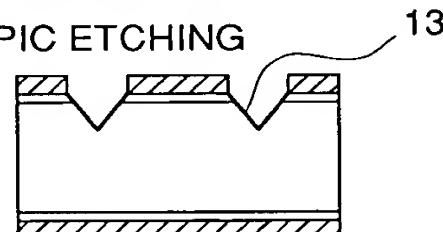


Fig. 3C

STRIPPING OXIDE FILM
FROM ENTIRE SURFACE

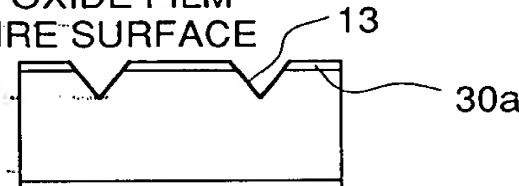


Fig. 3D

DIELECTRICALLY
SEPARATING OXIDE

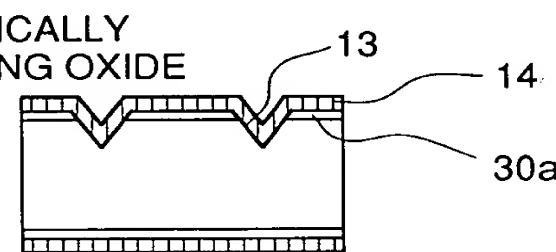


Fig. 3E

GROWING SEED
POLYSILICON FILM

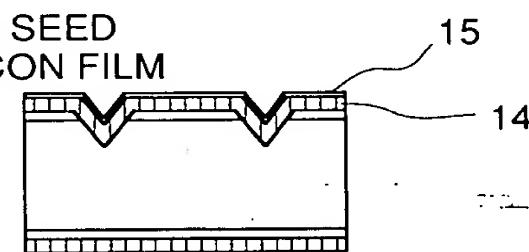
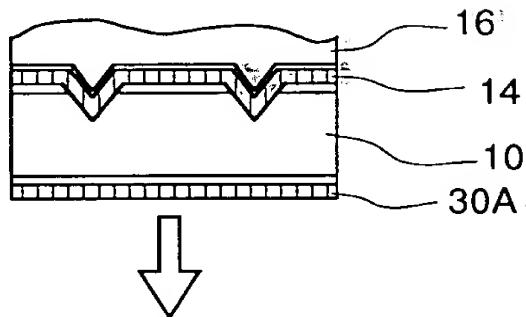


Fig. 3F

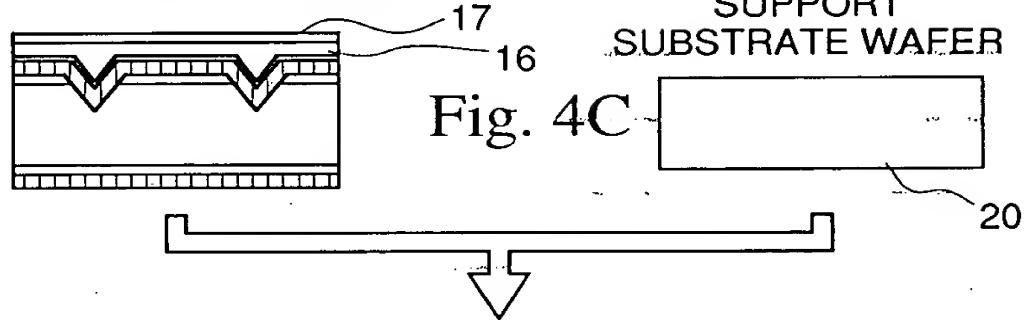
HIGH TEMPERATURE
POLYSILICON FORMATION

Fig. 4A



HIGH TEMPERATURE CVD
POLYSILICON GRINDING AND
POLISHING POLISHING
AFTER LOW TEMPERATURE
CVD POLYSILICON GROWTH

Fig. 4B



SUPPORT
SUBSTRATE WAFER

Fig. 4C

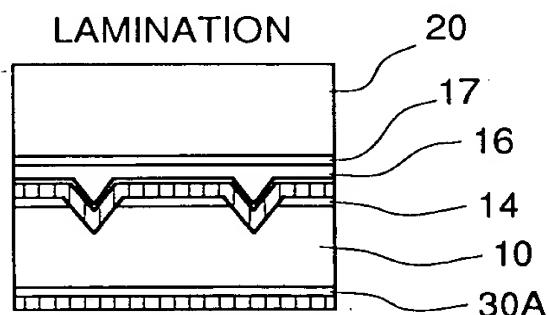
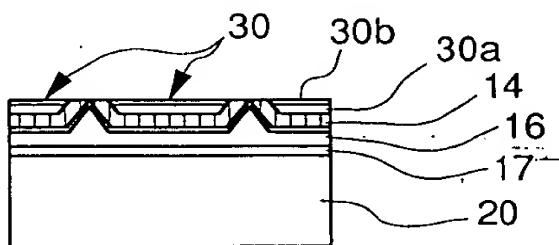


Fig. 4D



CHAMFERING PERIPHERY SEPARATION
GRINDING AND POLISHING

Fig. 4E



APPROVED	O.G. FIG.
BY	CLASS SUBCLASS
DRAFTSMAN	

Fig. 5A

P-TYPE OR N-TYPE(100)



Fig. 5B

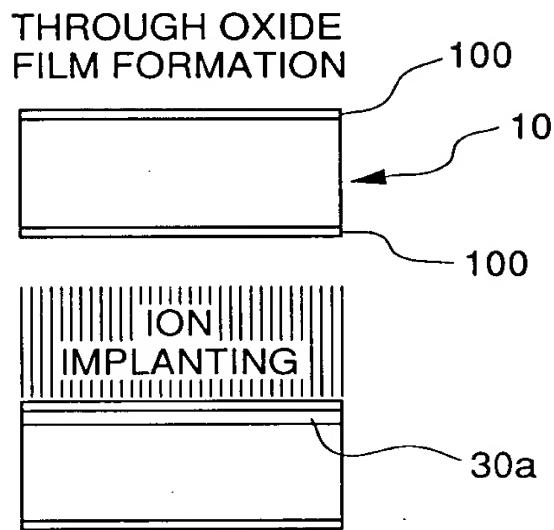


Fig. 5C

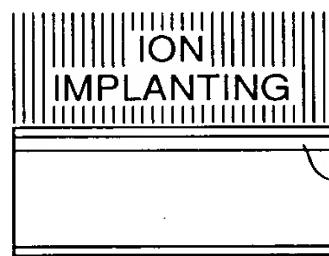


Fig. 5D

STRIPPING THE OXIDE FILM FROM THE WHOLE SURFACE

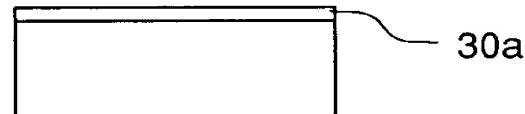


Fig. 5E

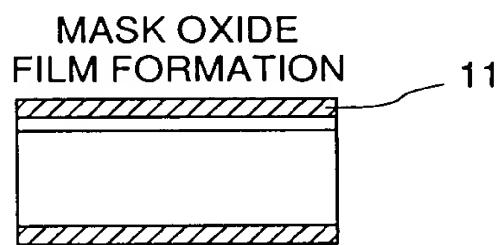
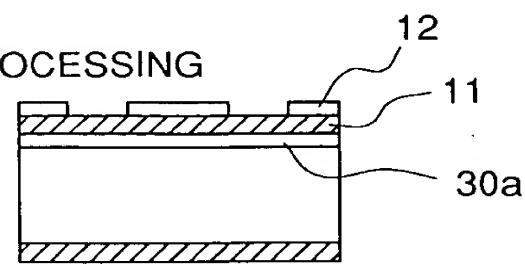
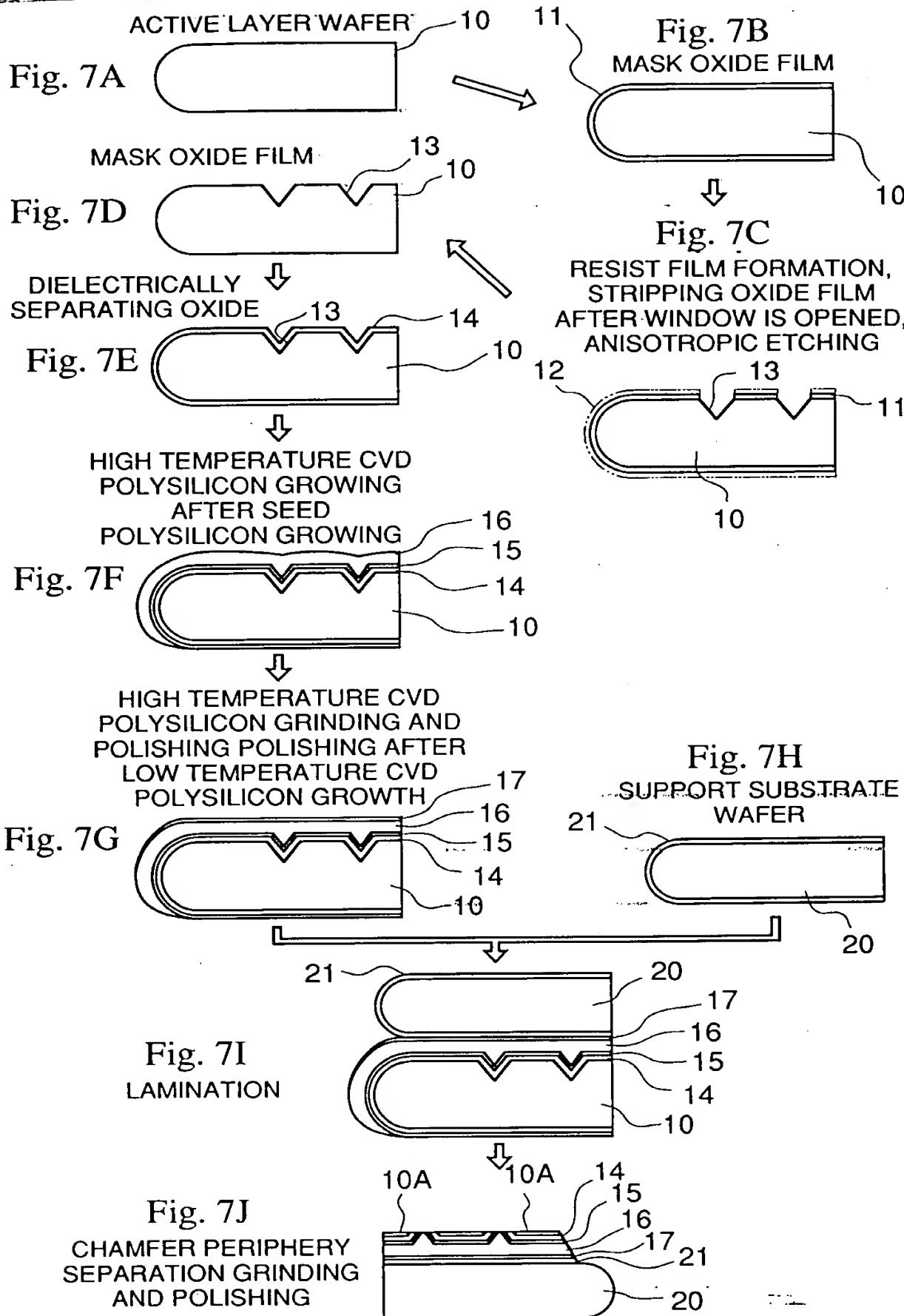


Fig. 5F

PR PROCESSING





APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig. 8

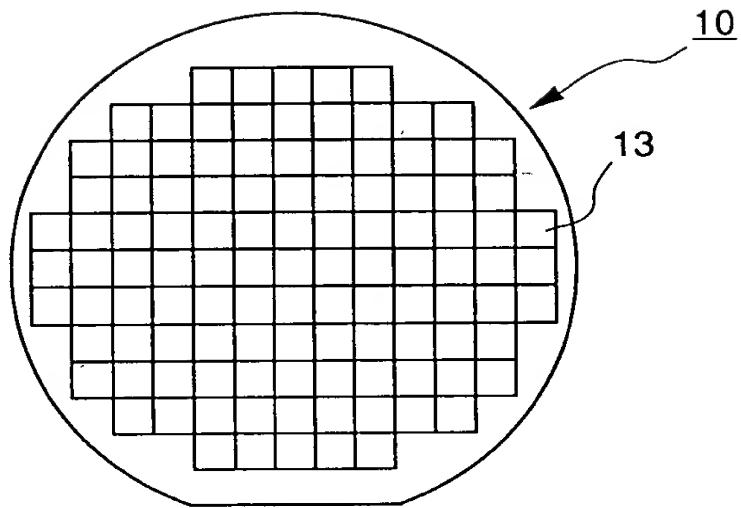
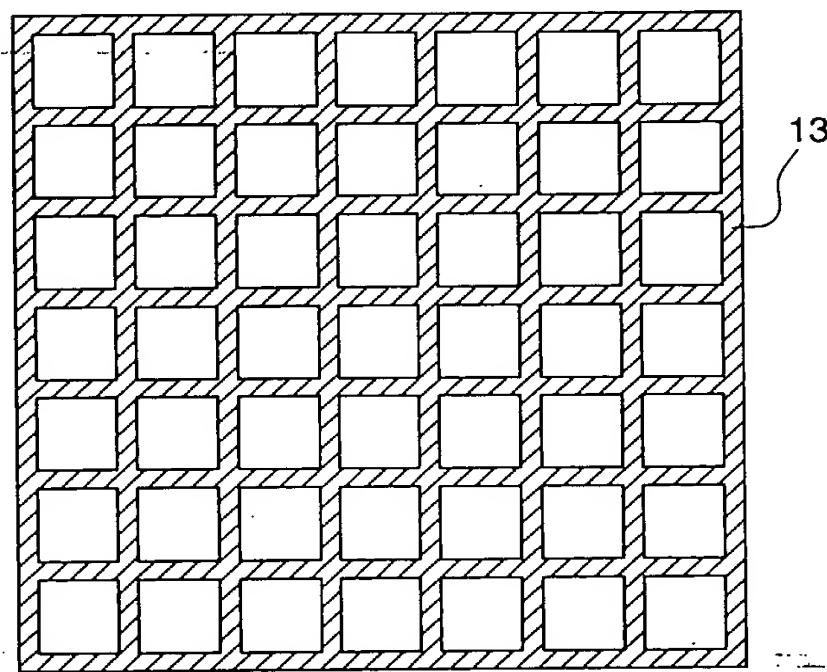
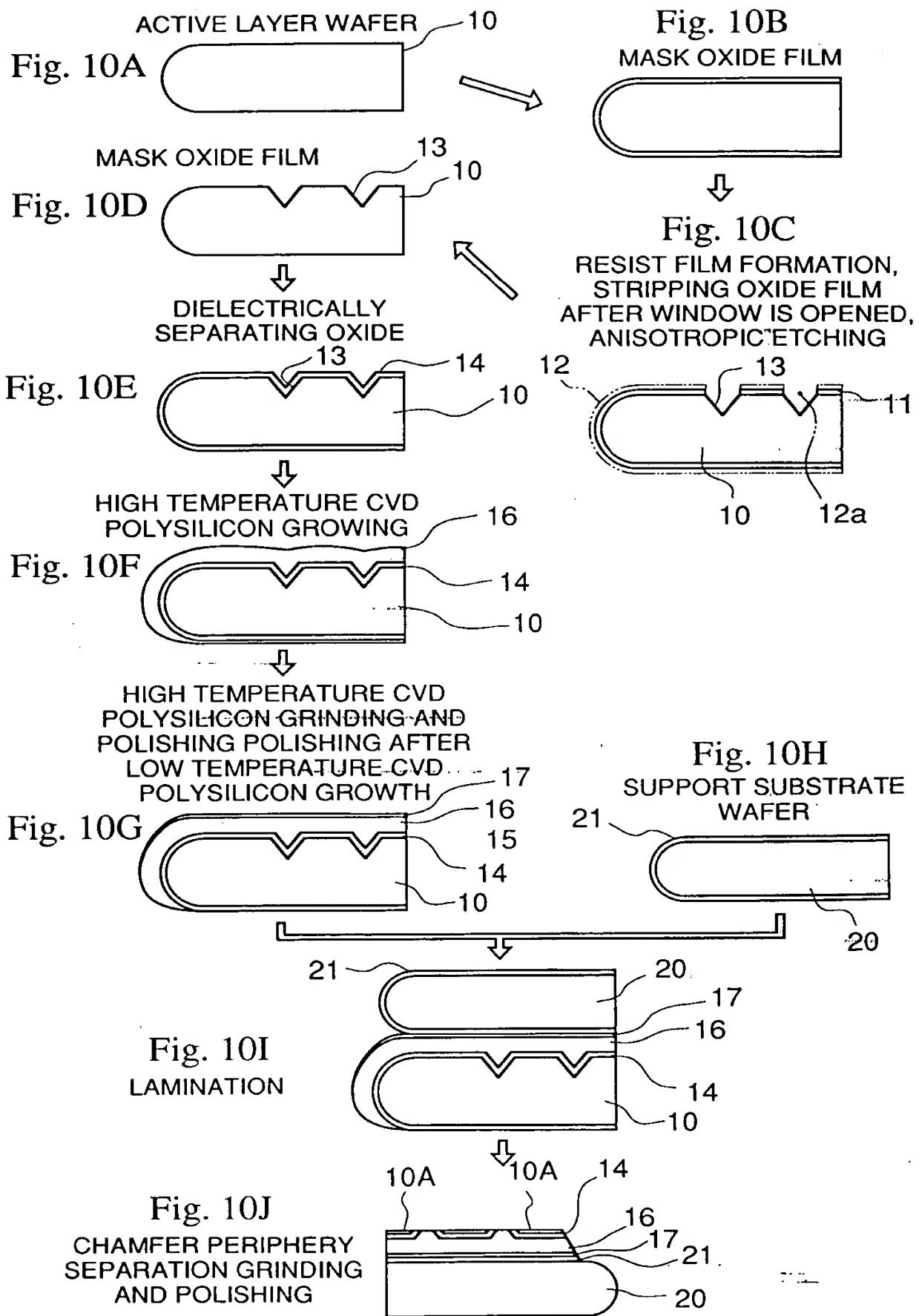


Fig. 9



APPROVED	J.C. FIG.
BY *	CLASS SUBCLASS
CRAFTSMAN	



APPROVED
BY C.G. FIG.
CLASS SUBCLASS
DRAFTSMAN

Fig. 11

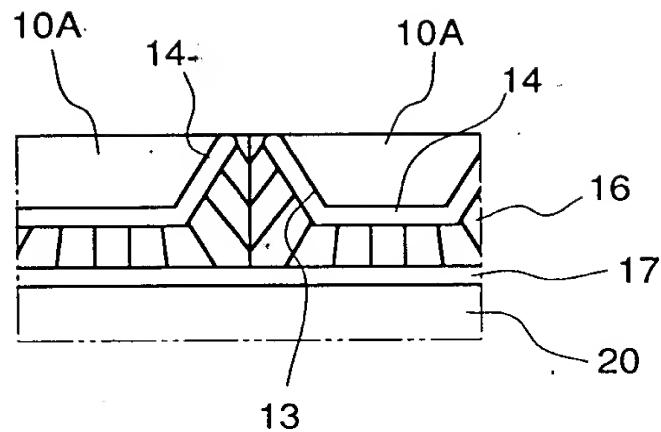
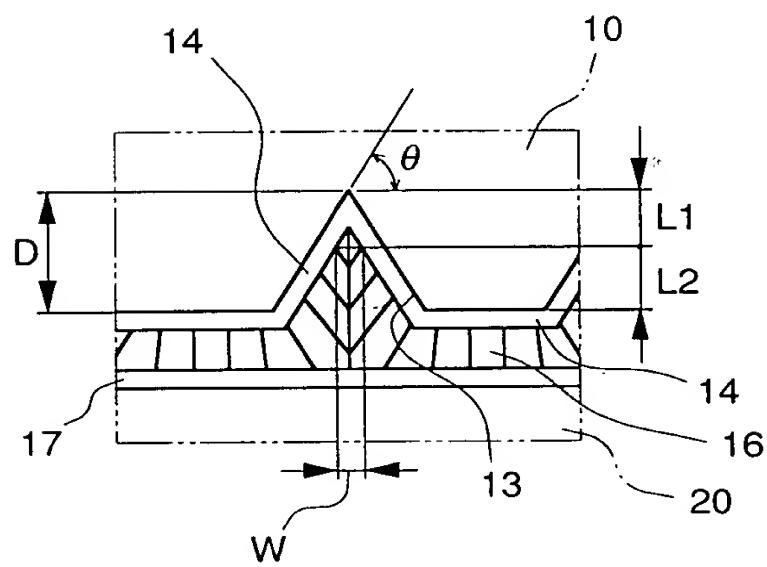


Fig. 12



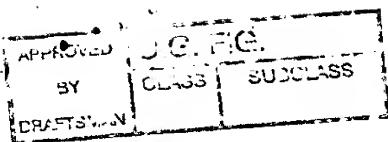


Fig. 13

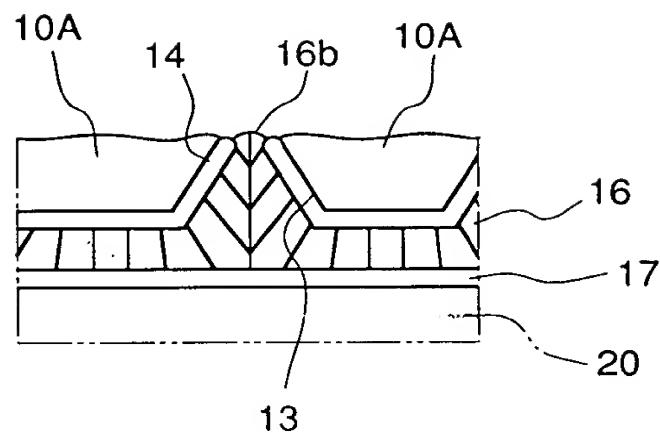
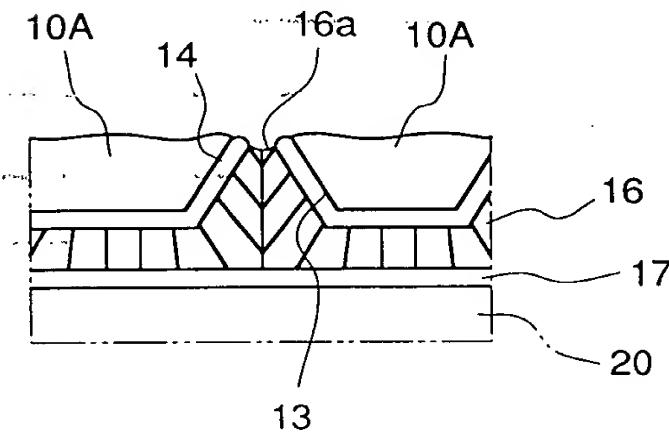
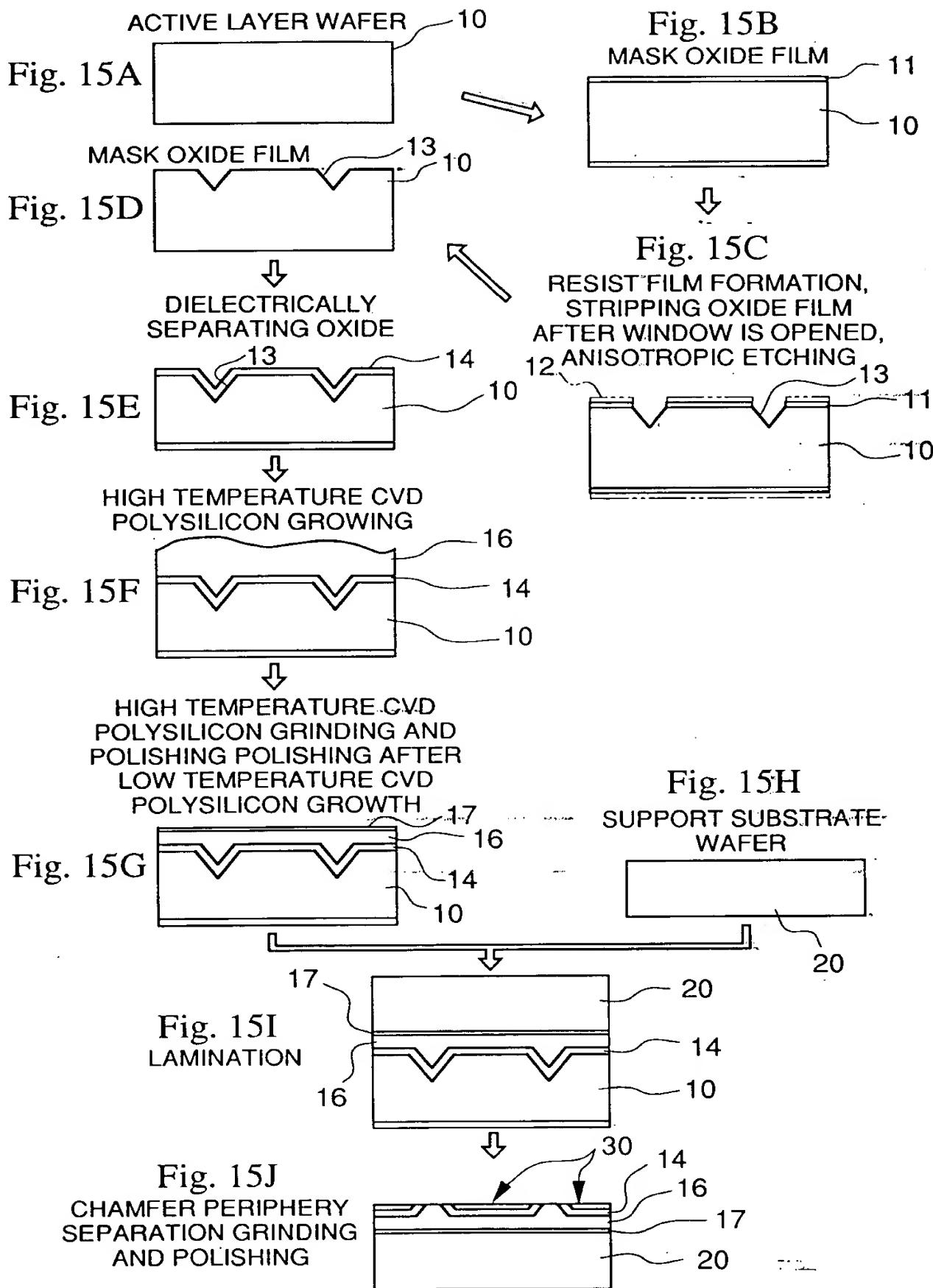
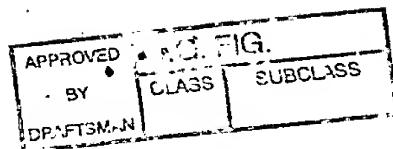


Fig. 14





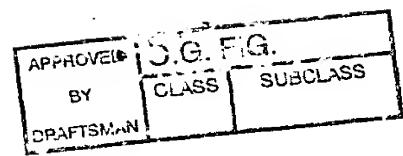


Fig. 16

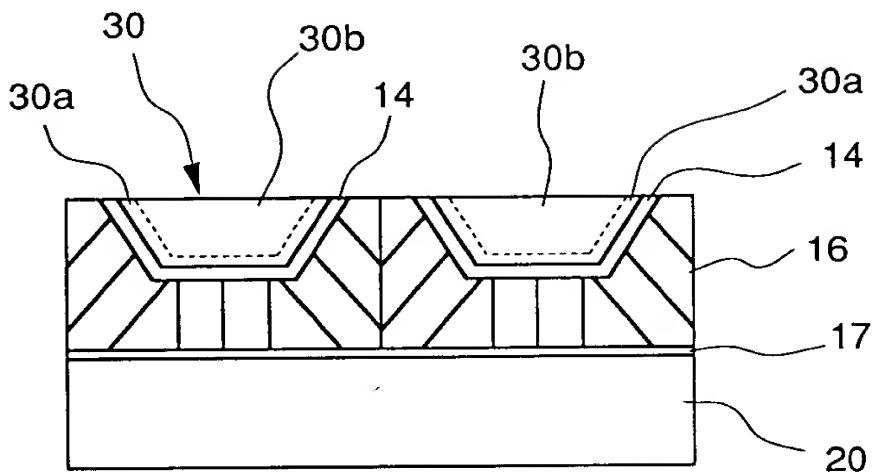


Fig. 17

